Attorney's Docket No. 42P18251

PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

G. Curello, et al.

Examiner: Unassigned

Application No.: 10/815,911

Art Unit: Unassigned

Filed:

March 31, 2004

For:

Enhancing Strained Device

Performance By Use Of Multi-

Narrow Section Layout

Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

## INFORMATION DISCLOSURE STATEMENT

Sir:

Enclosed is a copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 together with copies of the documents cited on that form, except for copies not required to be submitted (e.g., copies of U.S. patents and U.S. published patent applications need not be enclosed for applications filed after June 30, 2003). It is respectfully requested that the cited documents be considered and that the enclosed copy of Information Disclosure Citation Form PTO-1449 or PTO/SB/08 be initialed by the Examiner to indicate such consideration and a copy thereof returned to applicant(s).

Pursuant to 37 C.F.R. § 1.97, the submission of this Information

Disclosure Statement is not to be construed as a representation that a search

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Pursuant to 37 C.F.R. § 1.97, this Information Disclosure Statement is being submitted under one of the following (as indicated by an "X" to the left of the appropriate paragraph):

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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: April 20, 2004

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| Complete if Known      |                    |  |  |
|------------------------|--------------------|--|--|
| Application Number     | 10/815,911         |  |  |
| Filing Date            | March 31, 2004     |  |  |
| First Named Inventor:  | G. Curello, et al. |  |  |
| Art Unit               | Unassigned         |  |  |
| Examiner Name          | Unassigned         |  |  |
| Attorney Docket Number | 42P18251           |  |  |

|                       | U.S. PATENT DOCUMENTS |        |   |                                |  |   |
|-----------------------|-----------------------|--------|---|--------------------------------|--|---|
| Examiner<br>Initials* | Cite No.1             | Number | Document Number r-Kind Code <sup>2</sup> (If known) | Publication Date<br>MM-DD-YYYY | Name of Patentee or<br>Applicant of Cited Document | Pages, Columns,<br>Lines, Where<br>Relevant Passages<br>or Relevant Figures<br>Appear |
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|                                       | NON PATENT LITERATURE DOCUMENTS |   |  |  |  |
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| Examiner Cite No1                     |                                 | Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issunumber(s), publisher, city and/or country where published |  |  |  |
|                                       |                                 | RIM, Kern, et al., "Fabrication and Analysis of Deep Submicron Strained-Si N-MOSFET's," IEEE Transaction on Electron Devices, Vol. 47, No. 7, July 2000, pages 1406-1415  |  |  |  |
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| Examiner    | Date Considered |  |
| Signature   |                 |  |

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